

Innovation for the next generation

ML4030

CFP2 Loopback Module

10x10G CFP2 ports



Summary

The ML4030 is a CFP2 module compliance board which is a loopback module for high speed testing of CFP2 ports. The ML4030 is designed for 100 Gigabit Ethernet applications and provides 10 lanes of 10G each.

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Introduction

CFP2 Passive Loopback Module ML4030, is designed to provide an efficient and easy method of characterizing and testing 10x10G CFP2 ports. The ML4030 comes packaged in a MSA compliant shell housing offering excellent heat dissipation. It can be programmed to different power levels through an MDIO interface, thus emulating all CFP2 power classes.

Transmit data from the host is electrically routed, (internal to the loopback module), to the receive data outputs and back to the host. It provides an economical way to exercise CFP2 ports during R&D validation, production testing, and field testing.

Key Features

- Supports 10x10G
- CFP2 MSA Form Factor
- Built with advanced PCB material
- MDIO slave interface compliant with IEEE 802.3 Clause 45
- Custom memory maps
- MSA compliant functionality
- LED indicator
- Insertion counter
- Temperature Monitor and alarms warning
- RX_LOS-Alarm driven by TX_DIS
- Programmable Power dissipation up to 12W (can be customized for higher power)
- Cutoff temperature preventing module overheating

Applications

- 10x10G Electrical module testing and characterization
- CFP2 Port compliant testing

Ordering Information

Part Number	Description
ML4030	CFP2 Passive Loopback Module

Please contact us at sales@multilaneinc.com.